

ABSTRACT

A device for sending/receiving digital data capable of processing different bit rates, in particular in a VDSL environment.

The device for sending/receiving digital data is capable of processing different bit rates from a group of predetermined bit rates. It includes a channel coding/decoding stage including interleaving means MET and deinterleaving means MDET including a memory MM whose minimum size is fixed as a function of the maximum bit rate of said group and having a first memory space ESM1 assigned to the interleaving means and a second memory space ESM2 assigned to the deinterleaving means. The size of each of the two memory spaces is parameterable as a function of the bit rate actually processed by the device.

Reference : figure 6.